



**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q77404

Keiji HAYASHIDA, et al.

Appln. No.: 10/664,884

Group Art Unit: 2817

Confirmation No.: 4264

Examiner: Unknown

Filed: September 22, 2003

For: PHASE-LOCKED LOOP CIRCUIT REDUCING STEADY STATE PHASE ERROR

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 C.F.R. §§ 1.97 and 1.98**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and/or listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

1. European Patent Application Publication No. 0 299 742, published January 18, 1989.
2. United States Patent No. 5,087,829, published February 11, 1992.
3. United States Patent No. 5,670,903, published September 23, 1997.
4. United States Patent No. 5,883,534, published March 16, 1999.

One copy of each of the listed documents, other than any U.S. patents and patent publications, is submitted herewith, along with a copy of the European Search Report dated February 17, 2004, issued in the corresponding European Patent Application.

INFORMATION DISCLOSURE STATEMENT  
U.S. Appln. No.: 10/664,884

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date; (2) Before the mailing date of the first Office Action on the merits (whichever is later); or (3) Before the mailing date of the first Office Action after filing a request for continued examination (RCE) under §1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this paper is attached.

Respectfully submitted,



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WASHINGTON OFFICE  
23373  
CUSTOMER NUMBER

Date: April 16, 2004

Substitute for Form 1449 A & B/PTO		<i>Complete if Known</i>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>		Application Number	10/664,884
		Confirmation Number	4264
		Filing Date	September 22, 2003
		First Named Inventor	Keiji HAYASHIDA
		Art Unit	2817
		Examiner Name	Unknown
		Attorney Docket Number	Q77404

## U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

## NON-PATENT LITERATURE DOCUMENTS

Examiner Signature \_\_\_\_\_ Date Considered \_\_\_\_\_

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kind Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov), MPEP 901.04 or in the comment box of this document. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to indicate here if English language Translation is attached.